

-100mA / -50V Digital transistors (with built-in resistors)

DTA123JM / DTA123JE / DTA123JUA / DTA123JKA

勝 特 力 材 料 886-3-5753170 胜特力电子(上海) 86-21-34970699 胜特力电子(深圳) 86-755-83298787 Http://www.100y.com.tw

Applications

Inverter, Interface, Driver

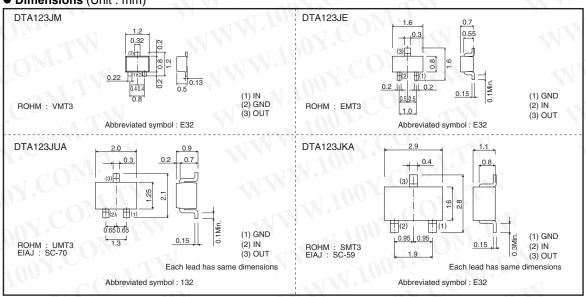
Features

- 1)Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2)The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- 3)Only the on/off conditions need to be set for operation, making the device design easy.

Structure

PNP epitaxial planar silicon transistor (Resistor built-in type)

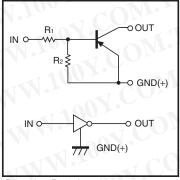
Dimensions (Unit : mm)



Packaging specifications

| -x1 10 | Package | VMT3 | EMT3 | UMT3 | SMT3 | |
|----------|------------------------------|--------|--------|-------------|--------|--|
| | Packaging type | Taping | Taping | Taping | Taping | |
| | Code | T2L | TL | T106 | T146 | |
| Part No. | Basic ordering unit (pieces) | 8000 | 3000 | 3000 | 3000 | |
| DTA123JM | | 0 | - | 41 - | _ | |
| DTA123JE | | - | 0 | N - | - | |
| DTA123JU | JA O | | - | | | |
| DTA123JK | DTA123JKA | | = 1 | | 0 | |

Inner circuit



R₁=2.2k Ω , R₂=47k Ω

• Absolute maximum ratings (Ta=25°C)

| Parameter | Symbol | Limits | | | |
|----------------------|----------|---------------------------------------|------|--|--|
| - arameter | Symbol | DTA123JM DTA123JE DTA123JUA DTA123JKA | Unit | | |
| Supply voltage | Vcc | -50 | V | | |
| Input voltage | Vin | -12 to +5 | V | | |
| Outrut august | lo | -100 | mA | | |
| Output current | IC(Max.) | -100 | | | |
| Power dissipation | Pp | 150 200 | mW | | |
| Junction temperature | Tj | 150 | °C | | |
| Storage temperature | Tstg | -55 to +150 | °C | | |

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● Electrical characteristics (Ta=25°C)

| Parameter | Symbol | Min. | Тур. | Max. | Unit | Conditions |
|----------------------|----------------|------|-------|------|------|----------------------------|
| N | VI(off) | - , | Of 1 | -0.5 | V | Vcc=-5V, Io=-100μA |
| Input voltage | VI(on) | -1.1 | 0.0 | - | | Vo=-0.3V, Io=-5mA |
| Output voltage | VO(on) | _ | -0.1 | -0.3 | V | Io/I:=-5mA/-0.25mA |
| Input current | lı | | 150 | -3.6 | mA | V=-5V |
| Output current | IO(off) | 34 | _ | -0.5 | μА | Vcc=-50V, VI=0V |
| DC current gain | Gı | 80 | c1 =1 | | | Vo=-5V, Io=-10mA |
| Input resistance | R ₁ | 1.54 | 2.2 | 2.86 | kΩ | XX |
| Resistance ratio | R2/R1 | 17 | 21 | 26 | 7- | 3 13 |
| Transition frequency | f⊤ * | (1 | 250 | | MHz | Vce=-10V, Ie=5mA, f=100MHz |

^{*} Characteristics of built-in transistor

Electrical characteristic curves

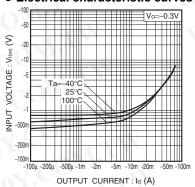


Fig.1 Input voltage vs. output current (ON characteristics)

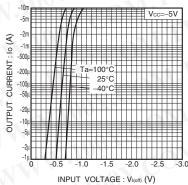


Fig.2 Output current vs. input voltage (OFF characteristics)

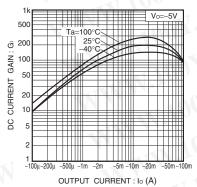


Fig.3 DC current gain vs. output

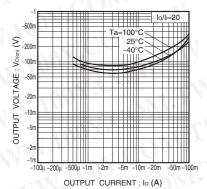


Fig.4 Output voltage vs. output current

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